

Dr. D. Y. Patil Unitech Society's Dr. D. Y. Patil Institute of Technology, Pimpri, Pune **Department of Electrical Engineering**

Activity: "Innovative Teaching Learning Pedagogy"

Date & Day: Thursday, 17/04/2025

Activity No 06

Type of Activity: Search word

Subject: FMA

Venue: Class Room no.B201

Activity conducted by

- Ms. Rajashree Bhokare

Objectives:

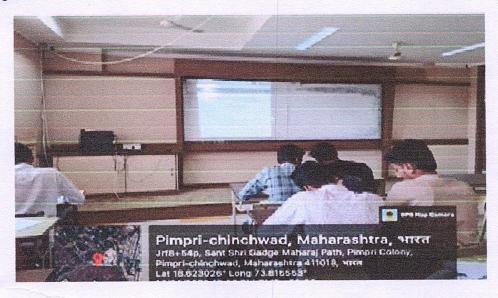
1. To help students recall and recognize key terms related to 8051 interrupts (e.g., IE, TCON, EX0, INT1, ISR, priority).

2. To enhance memory retention of interrupt-related concepts through active engagement with

terminology.

3. To introduce fun and interactive learning in a technical subject, making it more approachable and enjoyable.

Photographs:



Outcome:

- 1. Students exhibited careful attention to detail while locating and matching technical terms within the puzzle grid.
- 2. Students showed improved retention of interrupt concepts through participation in the interactive word search activity.

Ms. Rajashree Bhokare

SE Subject Teacher

Dr.Manasi P.Deore DAC

H.O.D



Dr. D. Y. Patil Unitech Society's

Dr. D. Y. Patil Institute of Technology

Department of Electrical Engineering

Activity: "Innovative Teaching Learning Pedagogy"

Mapping of Pedagogy with POs and PSOs:

PO1	PO2	РО3	PO4	PO5	PO6	P07	PO8	PO9	PO10	Po11	PO12	PSO1	PSO2	PSO3	
1	1						1	1	1			1	1		

Mapping of POs and PSOs with Justification:

POs and PSOs	Justification
Mapped	
PO1	Students apply fundamental concepts of microcontrollers while identifying and reinforcing technical terms.
PO2	Enhances cognitive skills through identification of embedded and microcontroller-related keywords.
PO8	Promotes disciplined, individual learning without malpractice during the activity.
PO9	Encourages healthy individual participation and, if done in groups, collaborative learning.
PO10	Encourages healthy individual participation and, if done in groups, collaborative learning.
PSO1	Reinforces key microcontroller-related concepts and terminology used in FMA.
PSO2	Students get familiar with the terminology that supports understanding microcontroller interfacing and simulation tools.

Rajashree Bhokare **Course Coordinator**

Dr. Manasi P.Deore

DAC

HOD