

Activity: "Innovative Teaching Learning Pedagogy"

Date & Day: Monday, 27/01/2025

Activity No 03

Type of Activity : Mind Map

Subject : FMA

Venue: Class Room no.B201

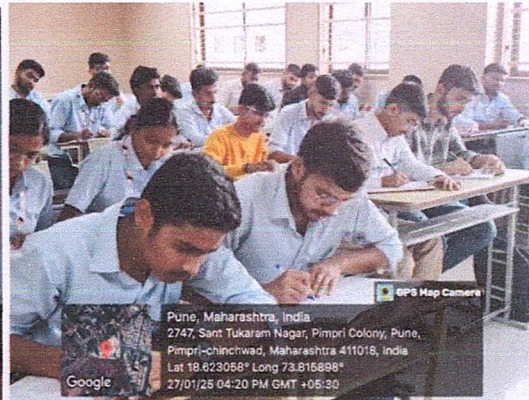
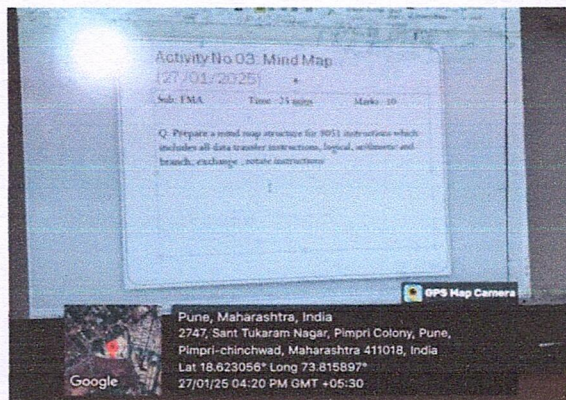
Activity conducted by

- Ms. Rajashree Bhokare

Objectives:

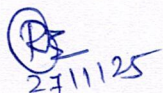
1. The activity helps students understand different categories of the 8051 instruction set, such as data transfer, arithmetic, logical, and control instructions.
2. By mapping instructions visually, students can develop logical thinking skills and understand how instructions interact with registers and memory.
3. The structured approach of a mind map simplifies complex concepts, making it easier for students to comprehend the 8051 instruction set.

Photographs:



Outcome:

1. By mapping out instructions, learners will gain a clearer understanding of how the 8051 microcontroller executes various instructions and their impact on system performance.
2. The activity will promote collaborative learning, allowing students to discuss and refine their knowledge through teamwork and peer interactions.



27/1/25

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Mapping of Pedagogy with POs and PSOs :


PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	Po11	PO12	PSO1	PSO2	PSO3
1	1	1		1					1		1	1	1	

Mapping of POs and PSOs with Justification:

POs and PSOs Mapped	Justification
PO1	Students recall and organize fundamental microcontroller concepts, showing foundational understanding.
PO2	Mind maps help students interlink topics like architecture, programming, and interfacing, supporting analytical thinking.
PO3	Students visualize system-level connections (e.g., ports, timers, ADC, GSM) leading to solution-oriented thinking.
PO5	Digital mind-mapping tools (like XMind, Coggle, or FreeMind) encourage the use of modern digital tools.
PO10	Creating and presenting mind maps improves students' ability to communicate technical information clearly.
PO12	Mind mapping enhances self-learning and organizational skills, promoting continuous learning habits.
PSO1	Mind mapping integrates electrical concepts (e.g., microcontroller peripherals, interfacing) with applications.
PSO2	The visual representation strengthens the understanding of system-level embedded design.


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