



Dr. D. Y. Patil Institute of Technology, Pimpri, Pune

Department of Electrical Engineering

Activity: "Innovative Teaching Learning Pedagogy"

Date & Day: 10/10/2024 Thursday

Activity Name: Match the Pair

Subject: PE

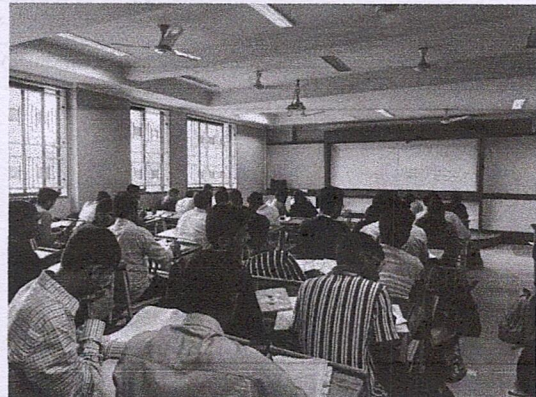
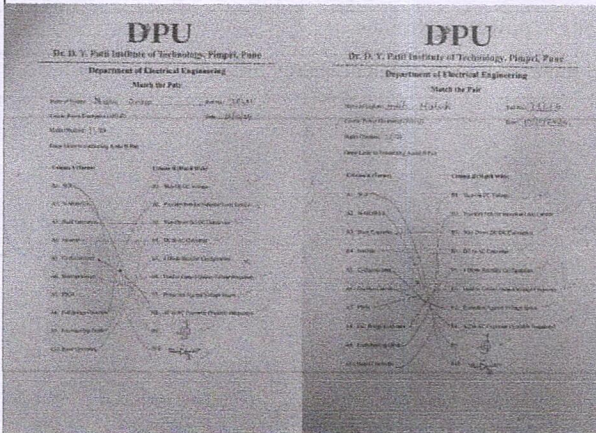
Venue: Classroom

Activity conducted by: Dr. Shankar D. Chavan

Objectives:

- To reinforce students' understanding of devices, converters, and their applications through an engaging matching activity.
- To encourage visual association and logical reasoning between terms, symbols, and functions.

Photographs:





Dr. D. Y. Patil Institute of Technology, Pimpri, Pune

Department of Electrical Engineering

Outcome:

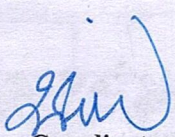
- Students will be able to identify, classify, and match power electronic devices and circuits with their correct characteristics or applications.
- Students will demonstrate critical thinking by linking theoretical concepts to real-world usage.

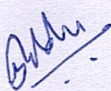
Mapping of Pedagogy with POs and PSOs:

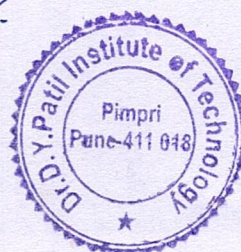
PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
3	2	-	-	-	-	-	-	1	1	-	2	2	2	-

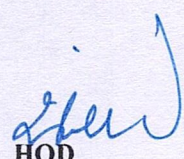
Mapping of POs and PSOs with Justification:

POs and PSOs Mapped	Justification
PO1	Strengthens fundamental knowledge of power electronic components.
PO2	Engages analytical skills to match function with device.
PO4	Encourages investigative thinking for device behaviour and use.
PO9	Fosters teamwork in pair/group solving.
PO10	Improves communication during peer discussions.
PO12	Promotes active, engaging, lifelong learning methods.
PSO1	Builds logical association skills in power electronics.
PSO2	Encourages application-level thinking tied to efficient energy use.


Course Coordinator


DAC




HOD